Mitsuo BABA
"Digital Control Variable Delay Circuit
Which is Hardly Susceptible to Noise"
Q77473----Filed September 11, 2003
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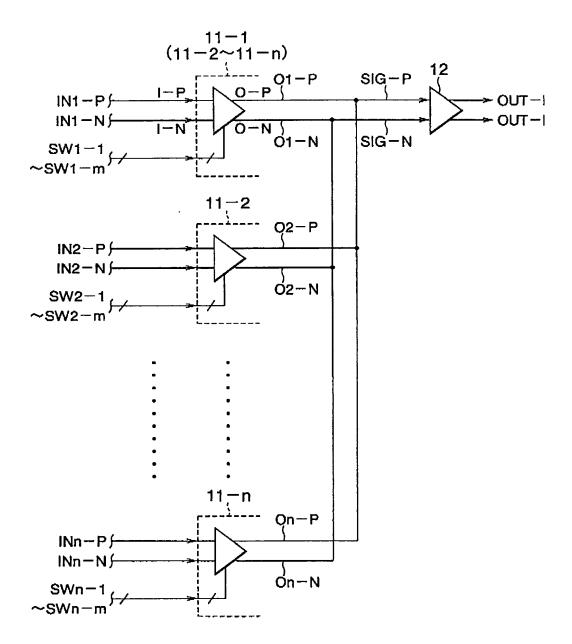


FIG. 1

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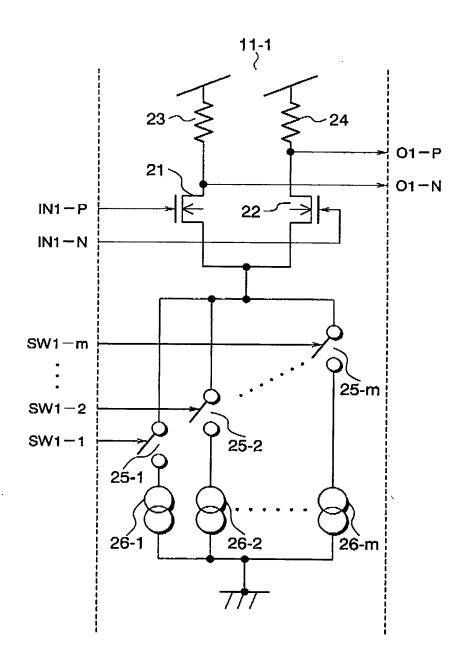


FIG. 2

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		STATE A	STATE B	STATE C	STATE D
	SW1-1	on	off	off	on 2
	SW1-2	on	on	off	on Signa
	SW1-3	on m	on	on	off
	:	: SIGN	VALS (m SIG	NALS : [(m−2)	:
	SW1-(m-1)	on	on	on Signal	.\$ off
	SW1-m	on	on)	on	off
اب	SW2-1	off	on $\}_{SIG}^1$	NAL on 2	off
SN S	SW2-2	off	off	on Signal	.\$ off
LS	SW2-3	off	off	off	off
윤	:	:	:	:	•
8	SW2-(m-1)	off	off	off	off
راد	SW2-m	off	off	off	off
IGIT/		off	off	off	off
n × m DIGITAL CONTROL SIGNAL		•	:	•	:
		•	:	:	:
		off	off	off	off
	SWn-1	off	off	off	off
	SWn-2	off	off	off	off
İ	SWn-3	off	off	off	on
	:	:	•	:	: (m-2)
	SWn-(m-1)	off	off	off	on SIGNAI
Į	_ SWn−m	off	off	off	on

FIG. 3

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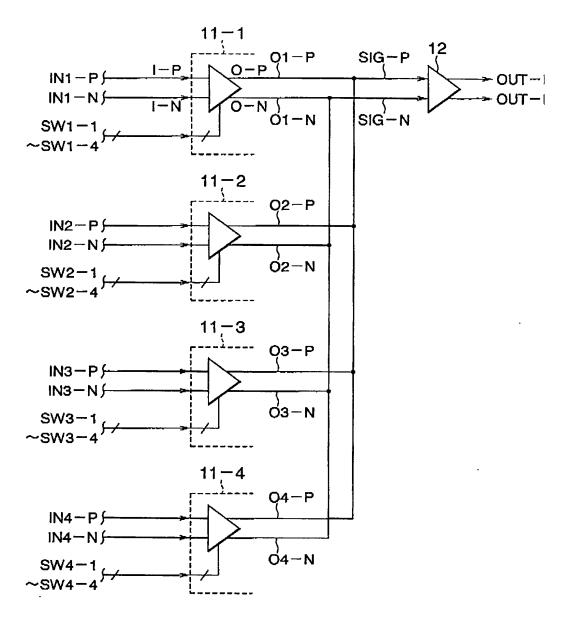
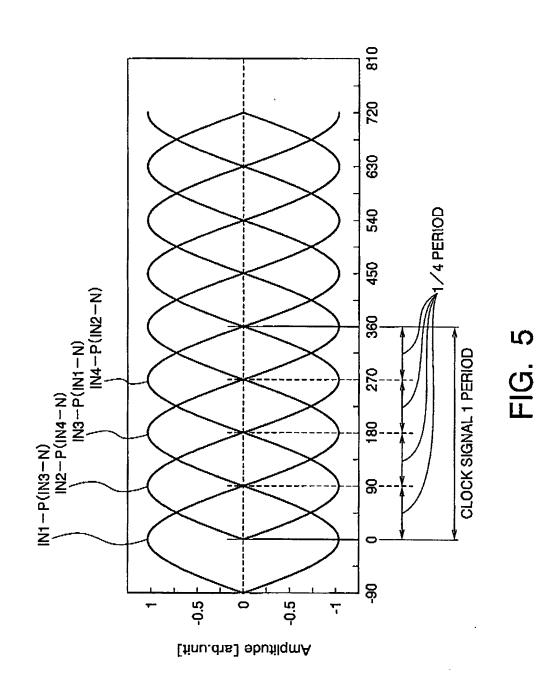


FIG. 4

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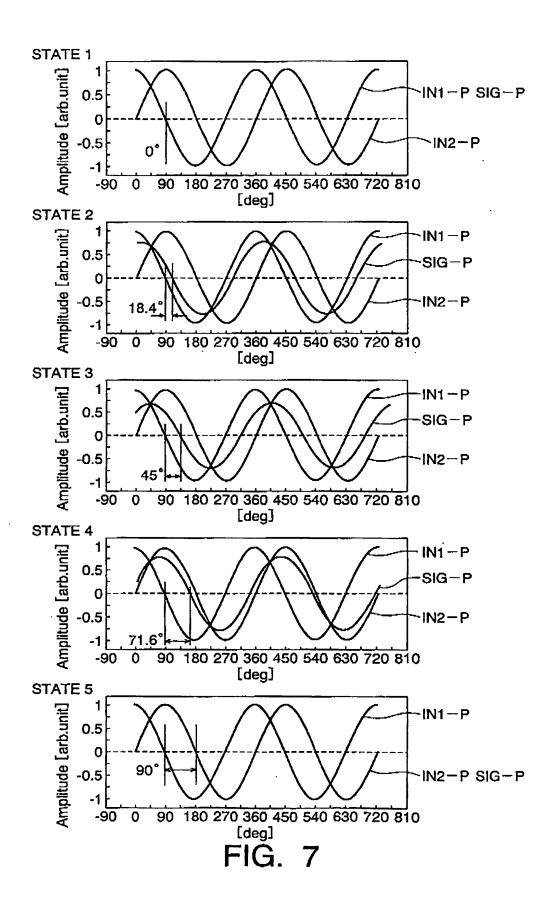


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		STATE1	STATE2	STATE3	STATE4	STATE5
× 4 DIGITAL CONTROL SIGNAL	SW1-1	on	off	off	off	off
	SW1-2	on 4	on	off	off	off
	SW1-3	on SIGN	VALS on Sigi	VALS on 2	off	off
	SW1-4	on)	on	on Sig	NALS on \sig	NAL Off
	SW2-1	off	on ${}^1_{SIG}$	NAL on 2	on	on
	SW2-2	off	off	on∫ ^{SIG}	NALS on SIG	NALS On 4
	SW2-3	off	off	off	on	on (SIGN.
	SW2-4	off	off	off	off	on
۲ 2	SW3-1	off	off	off	off	off
3IT/	SW3-2	off	off	off	off	off
)iii t	SW3-3	off	off	off	off	off
	\$W3-4	off	off	off .	off	off
4	SW4-1	off	off	off	off	off
	SW4-2	off	off	off	off	off
	SW4-3	off	off	off	off	off
	SW4-4	off	off	off	off	off

FIG. 6

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	STATE 1	, STATE 2	STATE 3
SW1-1	on /	off	off
SW1-2	on	on /	off
SW1-3	on	on /	on
SW1-4	on	on	on
SW2-1	off	/ on	, on
SW2-2	off	off	i/ on
SW2-3	off	off	off
SW2-4	off	off	off
SW3-1	off	off	off
SW3-2	off	off	off
SW3-3	off	off	off
SW3-4	off	off	off
SW4-1	off	off	off
SW4-2	off	off	off
SW4-3	off	off	off
SW4-4	off	off	off
	4 SIGNALS ARE ON (m NUMBER OF SIGNALS)	4 SIGNALS ARE ON (m NUMBER OF SIGNALS)	

5 SIGNALS ARE ON 5 SIGNALS ARE ON (m+1 NUMBER OF SIGNALS) (m+1 NUMBER OF SIGNALS)

FIG. 8

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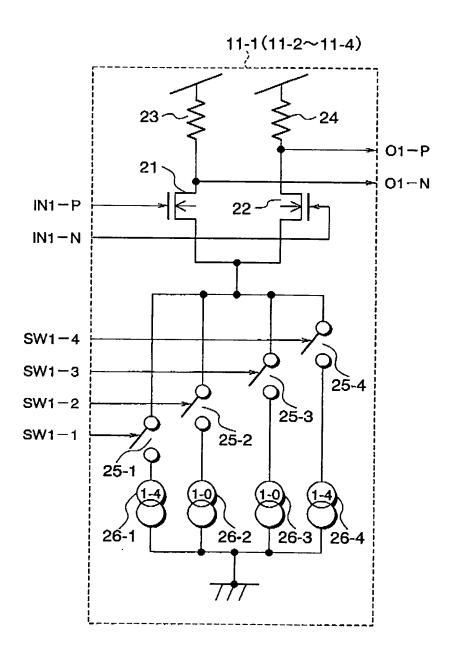


FIG. 9